



**IrDA Protocol IC Family: ACT-IR8200L, L-M, L-T, P Function Table (v1.25)**

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PART NO.		IR8200L	IR8200L-T	IR8200L-M	IR8200P	IR8240P	IR8250P
TYPE	ITEM						
Host Interface	DB-9P RS232 interface	✓	✓	✓	✓	✓	✓
	RS232 baud rate supported(bps)	1.2k~115.2k	1.2k~115.2k	1.2k~115.2k	1.2k~115.2k	9.6k	9.6k~115.2k
	CTS/RTS flow control	✓	✓	✓	✓	✓	✓
	Data Size	255 bytes FIFO	2048 bytes FIFO	255 bytes FIFO	64 bytes FIFO	64 bytes FIFO	64 bytes FIFO
	Ignore CTS/DSR	✓	✓	✓	✓	✓	✓
	XON/XOFF flow control	✓ (IrLPT only)	✓ (IrLPT only)	✓ (IrLPT only)	✓ (IrLPT only)	✓ (IrLPT only)	✓ (IrLPT only)
	Parameter Setting by SW (Runs on:Win98/NT/2K/XP)	✓	✓	✓			
	Parameter Setting by HW Jumpers	✓	✓	✓			
	Data format None, 8, 1	✓	✓	✓	✓	✓	✓
	Data format Even, 7, 1	✓	✓	✓	✓	✓	✓
IrDA Protocol Stack	IrDA Primary mode			✓	✓		
	IrDA Secondary mode	✓	✓	✓	✓	✓	✓
	IrDA Speed (bps)	9.6k~115.2k	9.6k~115.2k	9.6k~115.2k	9.6k~115.2k	9.6k~115.2k	9.6k~115.2k
	Max. PacketSize	128 bytes	2048 bytes	128 bytes	64 bytes	64 bytes	64 bytes
	Infrared encoder/decoder(IrPHY)	External Chip (IR220VACF)	External Chip (IR220VACF)	External Chip (IR220VACF)	Built-in	Built-in	Built-in
	IrLAP	✓	✓	✓	✓	✓	✓
	IrLMP	✓	✓	✓	✓	✓	✓
	IrLPT	✓	✓	✓(Secondary)	✓(Optional)	✓(Optional)	✓(Optional)



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	<b>TinyTP</b>	✓	✓	✓	✓(Optional)	✓(Optional)	✓(Optional)
	<b>IrCOMM 9wire cooked</b>	✓	✓	✓	✓(Optional)	✓(Optional)	✓(Optional)
	<b>IrCOMM 3wire raw</b>	✓	✓	✓(Secondary)	✓(Optional)	✓(Optional)	✓(Optional)
	<b>IrOBEX Transport</b>	✓	✓	✓(Secondary)	✓(Optional)	✓(Optional)	✓(Optional)
<b>Electrical Characteristics</b>	<b>Crystal Frequency</b>	22.1418MHz	22.1418MHz	22.1418MHz	7.3728MHz	7.3728MHz	7.3728MHz
	<b>Crystal Frequency for Infrared encoder/decoder</b>	3.6864MHz	3.6864MHz	3.6864MHz	No need	No need	No need
	<b>Supply voltage(VCC)</b>	3V to 5V	3V to 5V	3V to 5V	3V to 3.6V	3V to 3.6V	3V to 3.6V
	<b>Standby power consumption</b>	10mA	10mA	10mA	2uA	2uA	2uA
	<b>Active power consumption</b>	40mA	40mA	40mA	3mA	3mA	3mA
	<b>IC Package</b>	PLCC 44 pin /TQFP 44 pin	PLCC 44 pin /TQFP 44 pin	PLCC 44 pin /TQFP 44 pin	QFP 64 pin	SO 20 pin	SO 28 pin/ TSSOP 28 pin
<b>Cost</b>	<b>\$/Chip</b>	Medium	Medium	Medium	Lower	Lowest	Lower
	<b>Adapter/Evaluation Board</b>	<b>IR100SL</b>	<b>IR100SL</b>	<b>IR100SL</b>	<b>IR100SP</b>	IR8240/IR8250 Eval. Board	IR8240/IR8250 Eval. Board
	<b>Pre-tested Daughter PCB</b>	Yes	Yes	Yes	Yes	Yes	Yes
	<b>Adapter Interface Voltages</b>	5V, 12V, 30V	5V, 12V, 30V	5V, 12V, 30V	5V, 12V, 30V	5V, 12V, 30V	5V, 12V, 30V
	<b>IrDA-IrReady Certified</b>	Yes	Yes	Yes	Yes	Yes	Yes

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